



Docket No.: 21806-00106-US  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Gary S. Ditlow, et al.

#3

Application No.: 09/677,913

Group Art Unit: N/A

Filed: October 3, 2000

Examiner: Not Yet Assigned

For: METHOD OF POWER DISTRIBUTION  
ANALYSIS FOR I/O CIRCUITS IN ASIC  
DESIGNS

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Dear Sir:

In accordance with the Manual of Patent Examining Procedures, Section 601.03, please change the mailing address for all correspondence in the above-identified Patent application to:

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Dated:

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